



**J. Amarnath** :- DR.J.AMARNATH was graduated from Osmania University, College of Engineering, Hyderabad in the year 1982, M.E from Andhra University in the year 1984. After post graduation he joined as engineering officer in Tata Electric Company, Bombay in the year 1984 and worked about 3 years. Later he came for teaching in Andhra University and Nagarjuna University colleges for about 4 years and Later he joined as Assistant Professor in the Department of Electrical and Electronics Engineering in the year 1992. He obtained Ph.D in the area of High Voltage Engineering (Gas Insulated Substations) from J.N.T.University and BHEL corporate (R&D), Hyderabad in the year 2000. He is presently Professor, Electrical and Electronics Engineering Department, JNTU College of Engineering, Hyderabad.

**Pannala Krishna Murthy** :- Prof. PANNALA KRISHNA MURTHY has secured his B.Tech from SSGMCE- Shergoan. He obtained his masters degree from JNTU College of Engineering, kukatpally, Hyderabad. Presently he is working as Professor and Head of the Department in SBIT, Khammam. He got more than 12 Technical Publications at International level. He has been teaching variety of subjects like Electrical Circuits, Electrical Measurements, Electro Magnetic Fields, Power Systems, HVDC Transmission, etc. At present he is working on the problems of HVDC system fault analysis using wavelet transforms.